OPITIONAL CURRENT SENSE AMPLIFIER REPLACES U2

U1 ZXCT1009 **DNI**

R1 1.00K 0.1% **DNI**

C1 0.1 uF

U2 MAX4080T

IIN

R3 51.0K 0.1%

R4 20.0K 0.1%

VIN

R5 R6 R7 R9 R10 2.21 ohm 0.1%

TP1 RED

12V

J3 **+9V-14V .5A** POWER JACK P-5

J5 HEADER 3

Short pins 1-2 to source power from J3.
Short pins 2-3 to source power from the PSoC development kit (DVK)

EVIN VINP DVIN

TP2 BLACK

R8 ZERO

D1 SS22-E3/52T

TP3 RED

Digital Ground

Analog Ground

U3 ZXCT1009

I1

R12 1.00K 0.1%

R11 51.0K 0.1%

R13 51.0K 0.1%

V1

R14 2.00K 1%

C1

R16 422 ohm 1%

TP4 RED

R15 R17 R18 R19 R23 2.21 ohm 0.1%

Remove the jumper to disconnect all simulated loads.

J6 2 PIN HDR

R20 R21 R22 R24 100 ohm 1%

5V

J7 2 PIN HDR

Remove the jumper to disconnect the variable simulated load R26

R26 100 ohm

R29 100 ohm 1%

TV1

R25 121K 1%

R28 22.1K 1%

TP5 BLACK TP6 BLACK TP7 BLACK

U4 TPS54226 PWP

C2 22 uF

C3 3300 pF

C4 0.1 uF

L1 22 uH

C5 C6 22 uF

C7 1.0 uF 25V

C8 39 pF

D2 LED Green

R27 330 ohm

R30 100K

TR1

R31 20K

C9 0.1 uF

PLACE HEADER TO EDGE OF PCB
WITH FRICTION LOCK BODY TO INSIDE OF BOARD

J2 5 PIN HDR

D5V R2 ZERO DGND /ALERT P12.2 SCL P12.0 SDAT P12.1

J1 & J4 Reference PSoC port pins
CY8CKIT-001 DVK EBK Port A

J1 2 x 20 Pin Header RA

EN4 EN2 IIN C4 C2 TR4 TR2 I4 I3 I2 I1 SDAT DVIN

P3.7 P3.5 P3.3 P3.1 P4.7 P4.5 P4.3 P4.1 P12.3 P12.1 D3V3 DGND VIN

P3.6 P3.4 P3.2 P3.0 P5.6 P5.4 P5.2 P5.0 P4.6 P4.4 P4.2 P4.0 P12.2 P12.0 VADJ D5V DGND

EN3 EN1 VIN C3 C1 TR3 TR1 V4 V3 V2 V1 /ALERT SCL

J4 2 x 20 Pin Header **DNI**

EN4 EN2 IIN C4 C2 TR4 TR2 I4 I3 I2 I1 SDAT DVIN

P3.7 P3.5 P3.3 P3.1 P5.7 P5.5 P5.3 P5.1 P4.7 P4.5 P4.3 P4.1 P12.3 P12.1 D3V3 DGND VIN

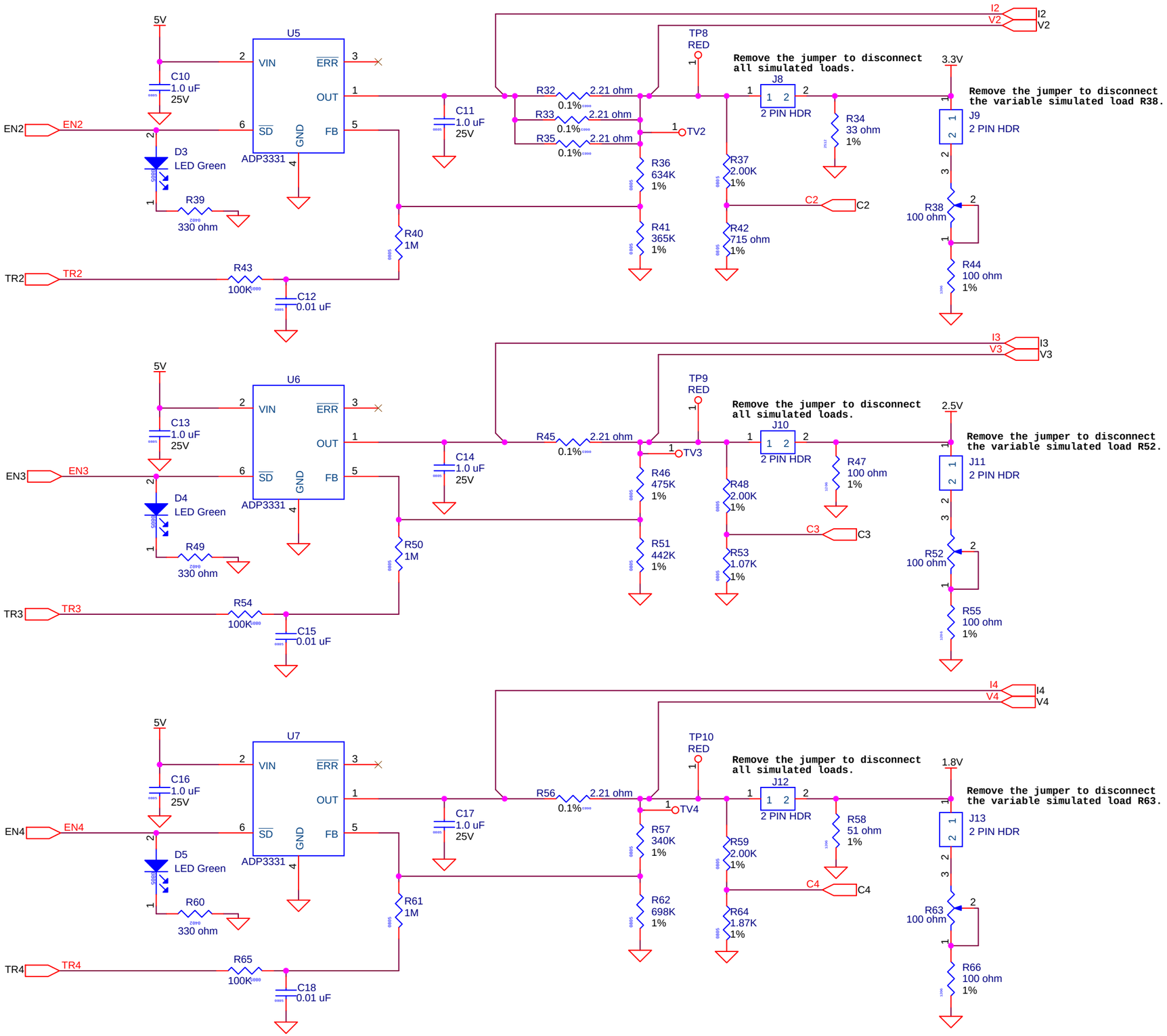
P3.6 P3.4 P3.2 P3.0 P5.6 P5.4 P5.2 P5.0 P4.6 P4.4 P4.2 **P4.0** P12.2 P12.0 VADJ D5V DGND

EN3 EN1 VIN C3 C1 TR3 TR1 V4 V3 V2 V1 /ALERT SCL

PCB#-PDC-09606
ASSY#-120-09606-0

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PCB#-PDC-09606
ASSY#-120-09606-0

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Title: **CY8CKIT-035 PSoC Power Management EBK**

Size	Document Number	Rev
B	**REF-15113**	**

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